FIG. 1

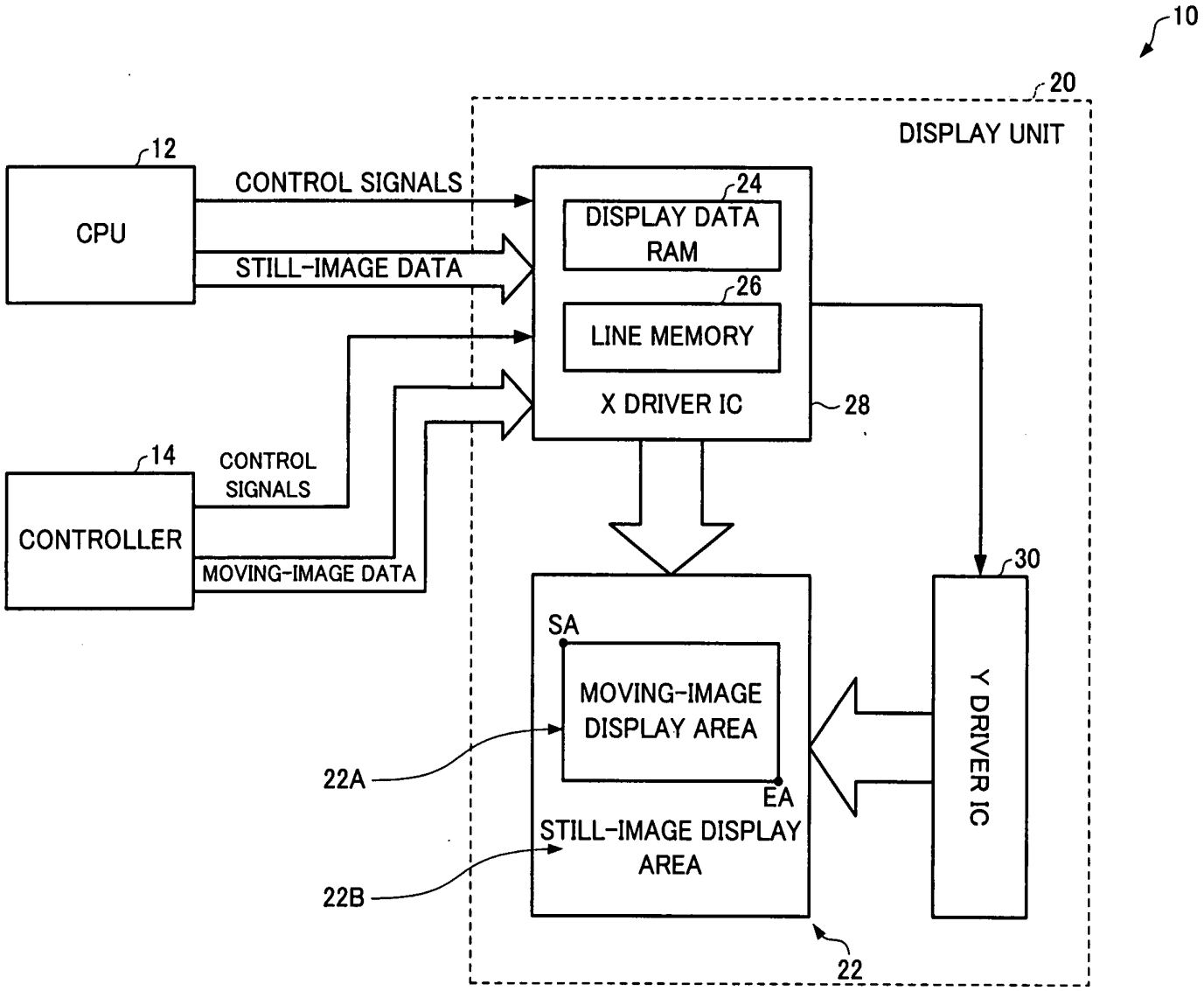

10
20
DISPLAY UNIT
12
CPU
CONTROL SIGNALS
STILL-IMAGE DATA
24
DISPLAY DATA RAM
26
LINE MEMORY
X DRIVER IC
28
14
CONTROLLER
CONTROL SIGNALS
MOVING-IMAGE DATA
30
SA
MOVING-IMAGE DISPLAY AREA
22A
EA
STILL-IMAGE DISPLAY AREA
22B
Y DRIVER IC
22

FIG. 2

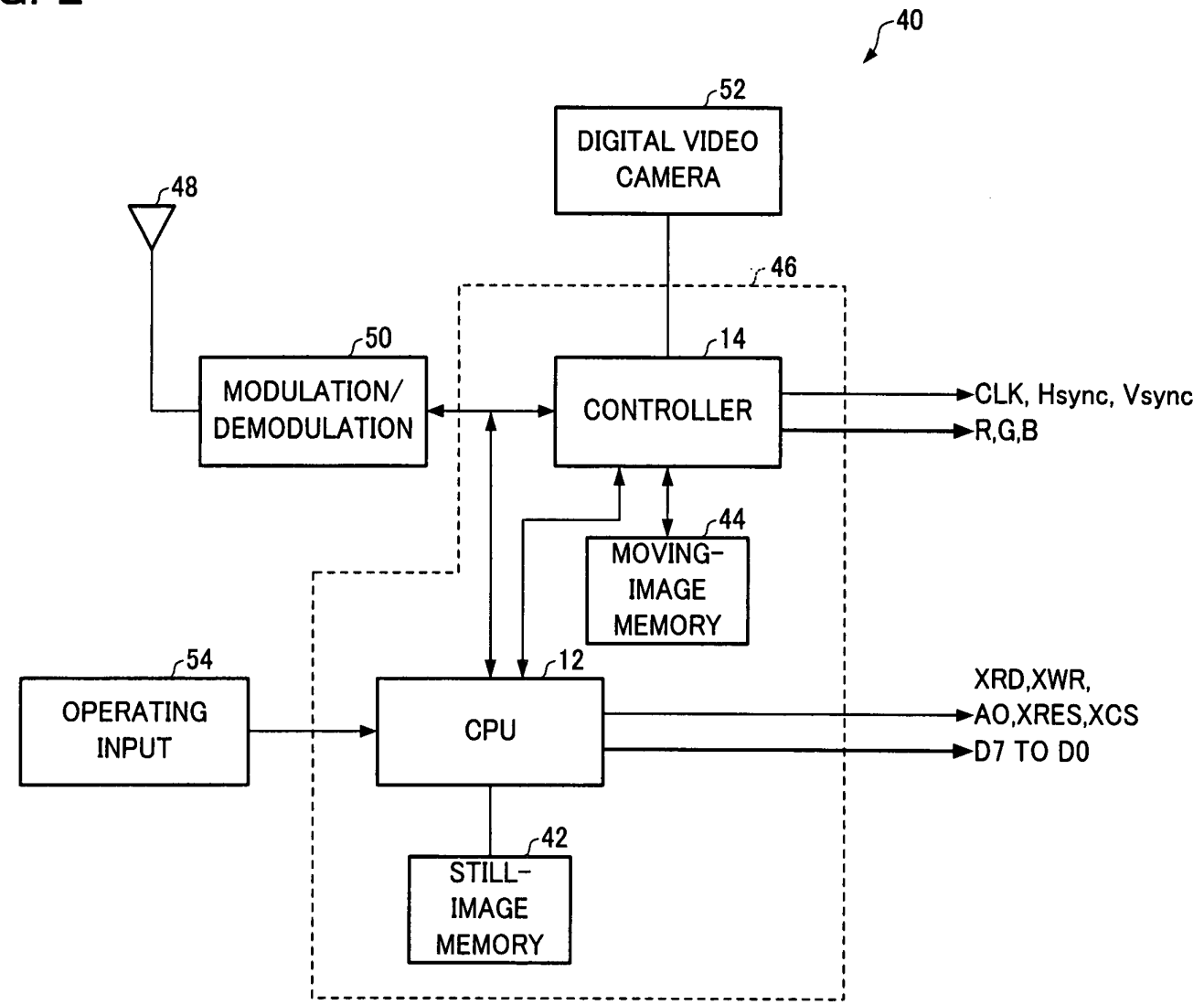

40
52
DIGITAL VIDEO CAMERA
48
46
50
MODULATION/ DEMODULATION
14
CONTROLLER
CLK, Hsync, Vsync
R,G,B
44
MOVING-IMAGE MEMORY
54
OPERATING INPUT
12
CPU
XRD,XWR, AO,XRES,XCS
D7 TO D0
42
STILL-IMAGE MEMORY

FIG. 3

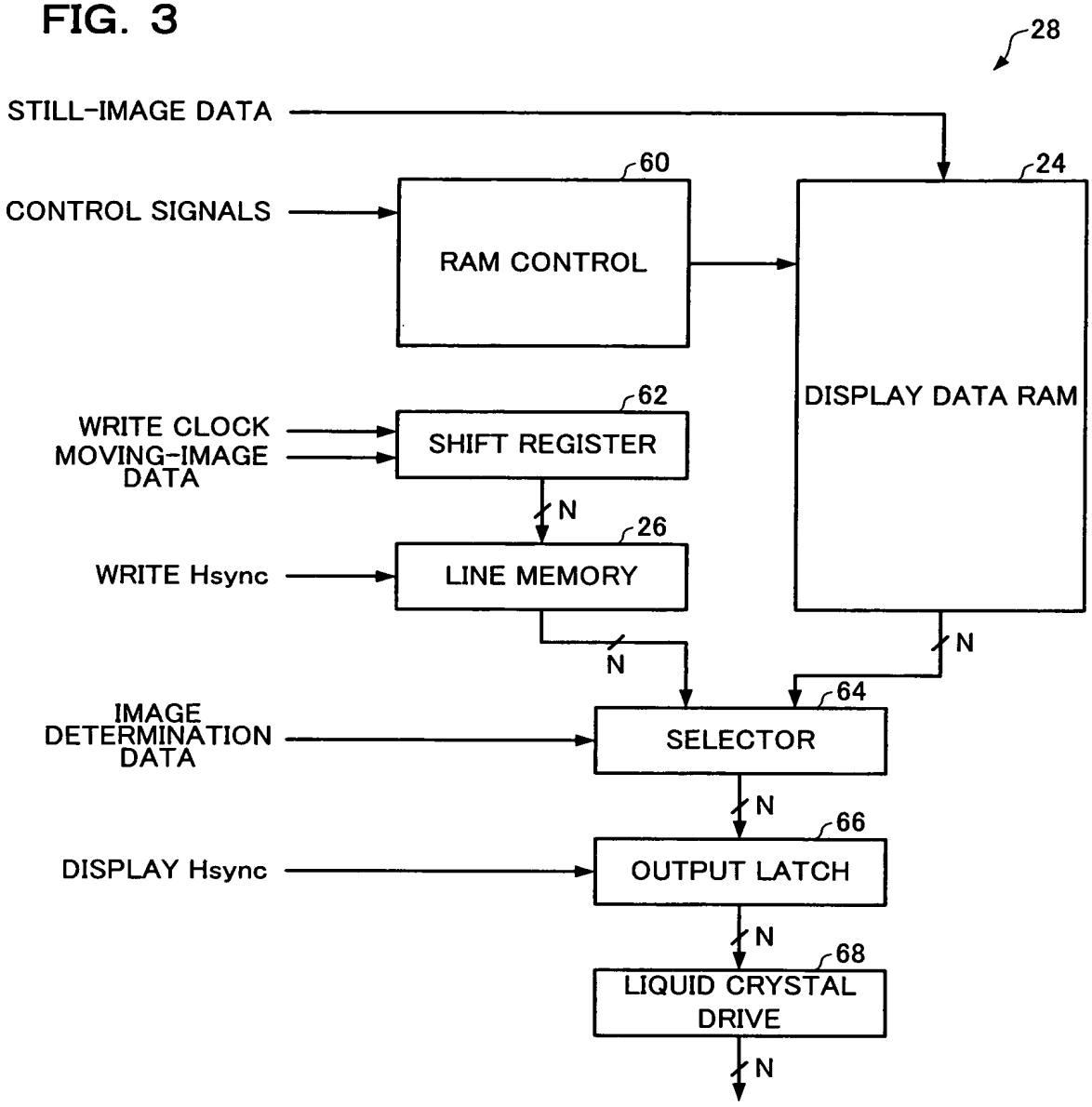

28
STILL-IMAGE DATA
CONTROL SIGNALS
60
RAM CONTROL
24
DISPLAY DATA RAM
WRITE CLOCK
MOVING-IMAGE DATA
62
SHIFT REGISTER
N
WRITE Hsync
26
LINE MEMORY
N
N
IMAGE DETERMINATION DATA
64
SELECTOR
N
DISPLAY Hsync
66
OUTPUT LATCH
N
68
LIQUID CRYSTAL DRIVE
N

FIG. 4

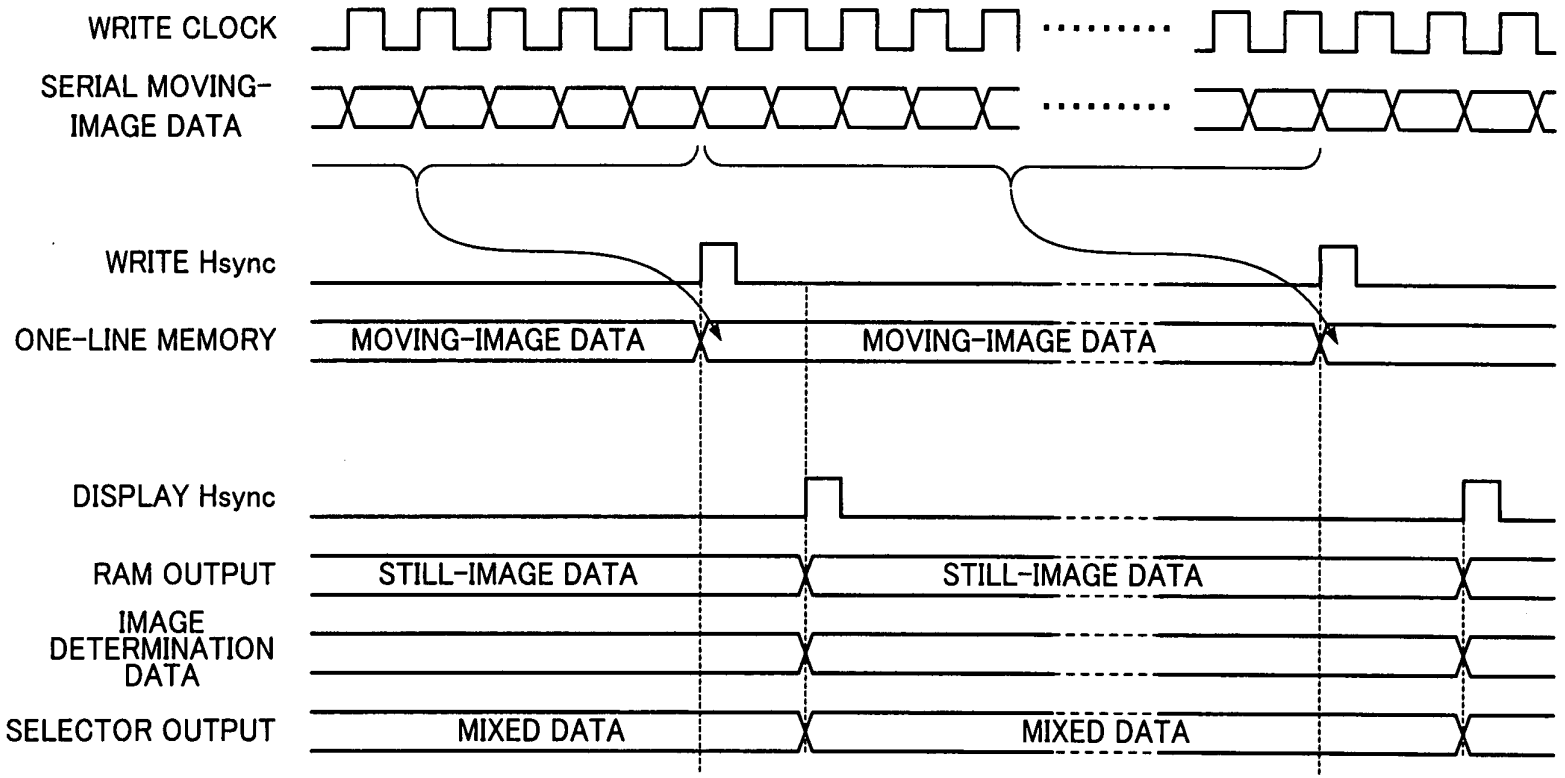

WRITE CLOCK
SERIAL MOVING-IMAGE DATA
WRITE Hsync
ONE-LINE MEMORY
MOVING-IMAGE DATA
MOVING-IMAGE DATA
DISPLAY Hsync
RAM OUTPUT
STILL-IMAGE DATA
STILL-IMAGE DATA
IMAGE DETERMINATION DATA
SELECTOR OUTPUT
MIXED DATA
MIXED DATA

FIG. 5

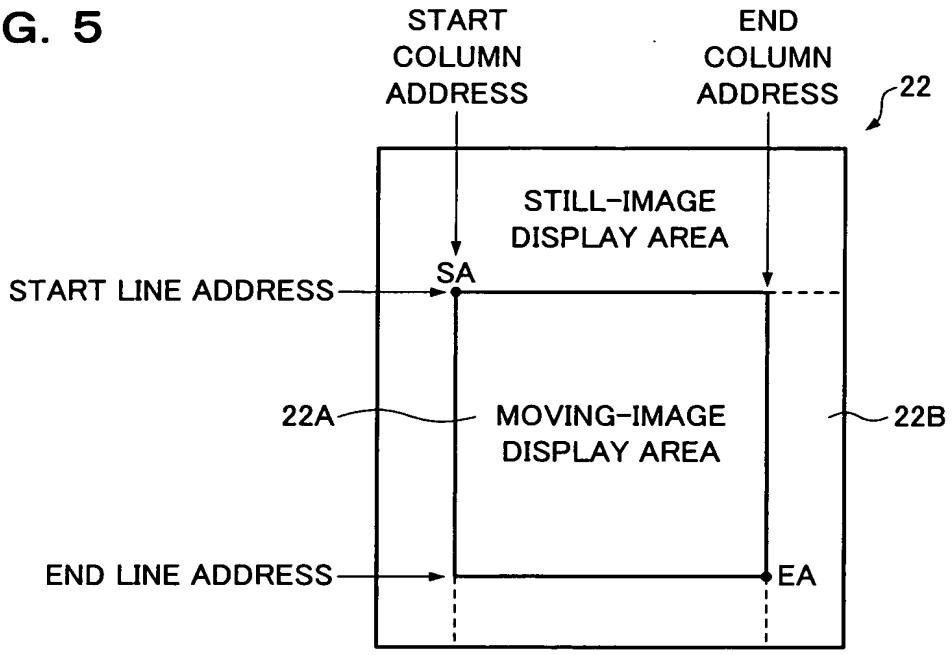

START COLUMN ADDRESS
END COLUMN ADDRESS
22
STILL-IMAGE DISPLAY AREA
SA
START LINE ADDRESS
22A
MOVING-IMAGE DISPLAY AREA
22B
END LINE ADDRESS
EA

FIG. 6

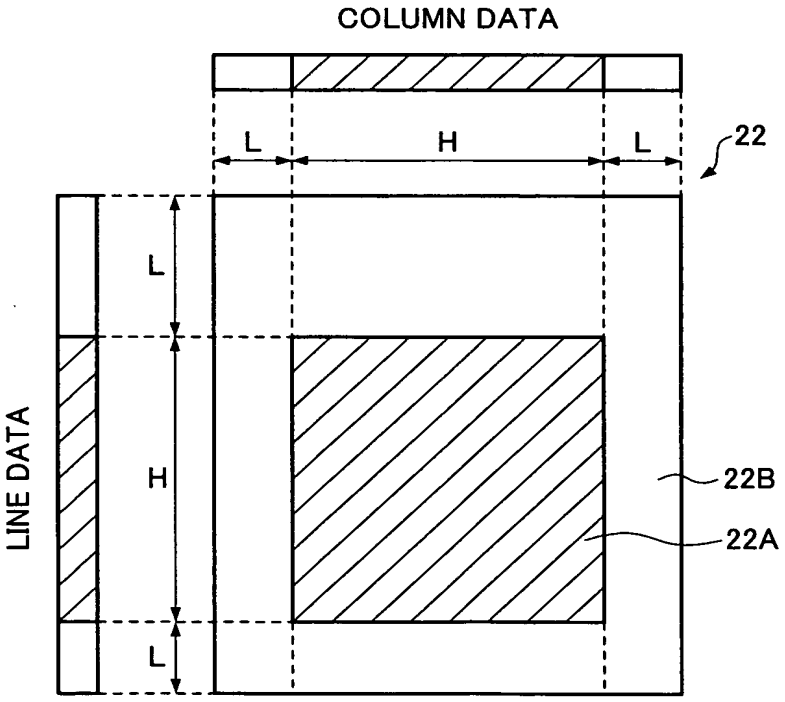

COLUMN DATA
L
H
L
22
L
H
L
LINE DATA
22B
22A

FIG. 7A

LINE DATA	COLUMN DATA	SELECTOR OUTPUT
H	H	MOVING-IMAGE DATA
H	L	STILL-IMAGE DATA
L	H	STILL-IMAGE DATA
L	L	STILL-IMAGE DATA

FIG. 7B

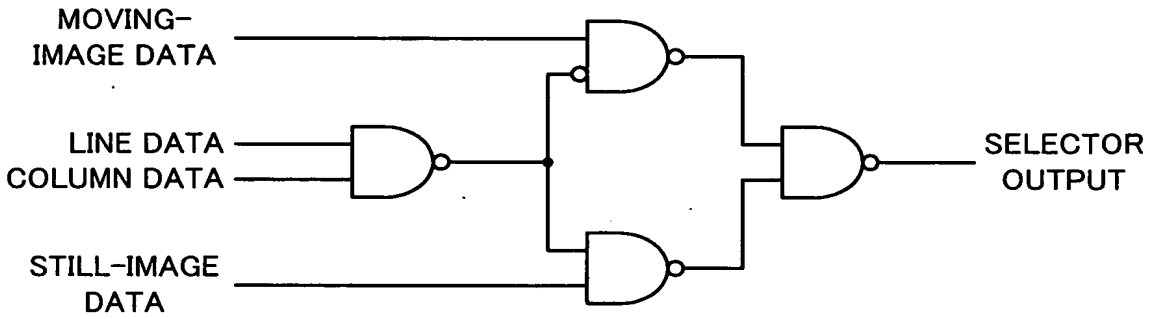


FIG. 8

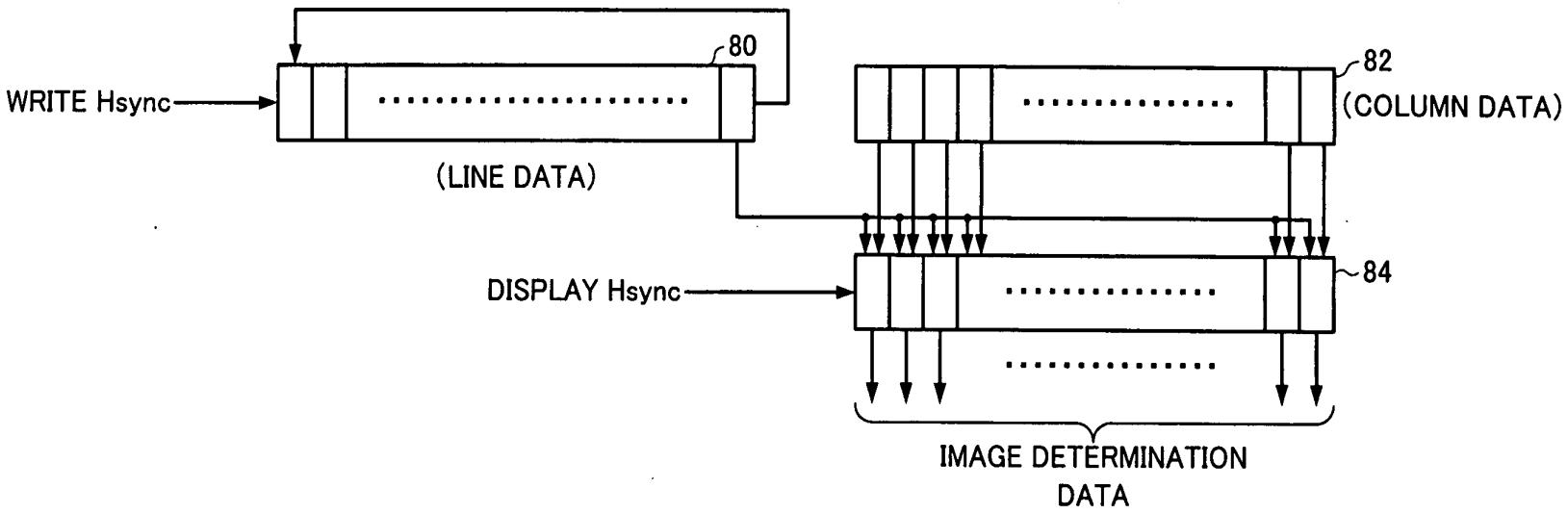

WRITE Hsync
80
(LINE DATA)
82
(COLUMN DATA)
DISPLAY Hsync
84
IMAGE DETERMINATION DATA

FIG. 9

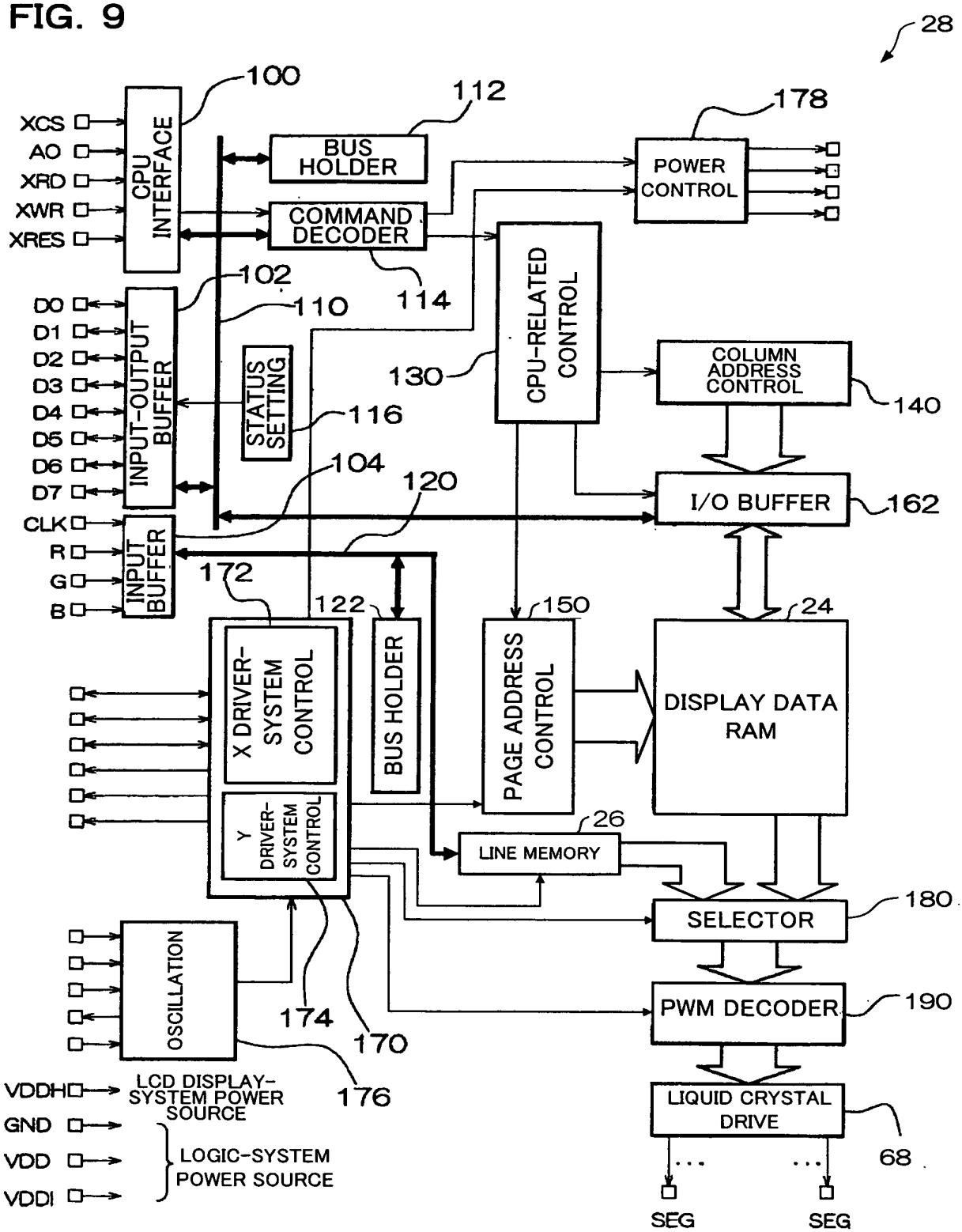

28
100
112
178
XCS
AO
XRD
XWR
XRES
CPU INTERFACE
BUS HOLDER
COMMAND DECODER
POWER CONTROL
114
102
110
D0
D1
D2
D3
D4
D5
D6
D7
INPUT-OUTPUT BUFFER
STATUS SETTING
116
CPU-RELATED CONTROL
130
COLUMN ADDRESS CONTROL
140
104
120
I/O BUFFER
162
CLK
R
G
B
INPUT BUFFER
172
122
150
24
X DRIVER-SYSTEM CONTROL
Y DRIVER-SYSTEM CONTROL
BUS HOLDER
PAGE ADDRESS CONTROL
DISPLAY DATA RAM
26
LINE MEMORY
180
SELECTOR
OSCILLATION
174
170
PWM DECODER
190
VDDH
LCD DISPLAY-SYSTEM POWER SOURCE
176
GND
VDD
VDDI
LOGIC-SYSTEM POWER SOURCE
LIQUID CRYSTAL DRIVE
68
SEG
SEG

FIG. 10

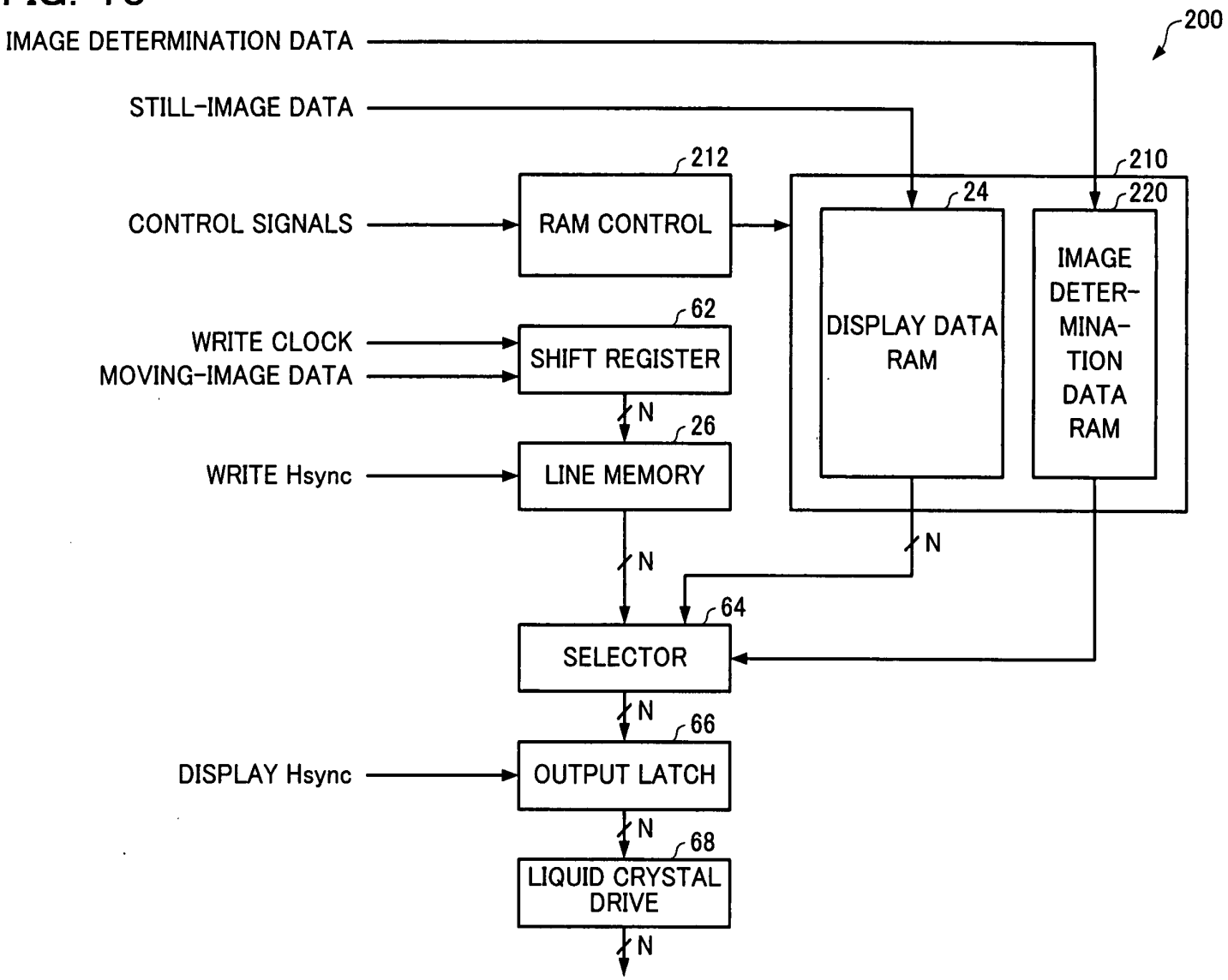

200
IMAGE DETERMINATION DATA
STILL-IMAGE DATA
CONTROL SIGNALS
WRITE CLOCK
MOVING-IMAGE DATA
WRITE Hsync
DISPLAY Hsync
212
RAM CONTROL
62
SHIFT REGISTER
26
LINE MEMORY
64
SELECTOR
66
OUTPUT LATCH
68
LIQUID CRYSTAL DRIVE
210
24
DISPLAY DATA RAM
220
IMAGE DETER-MINA-TION DATA RAM
N

FIG. 11A

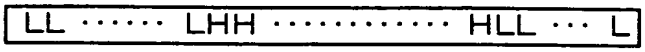


FIG. 11B

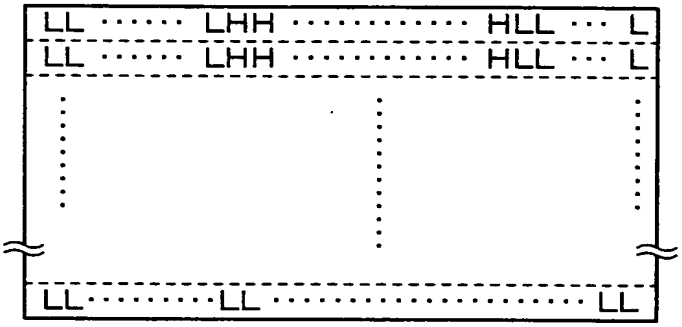
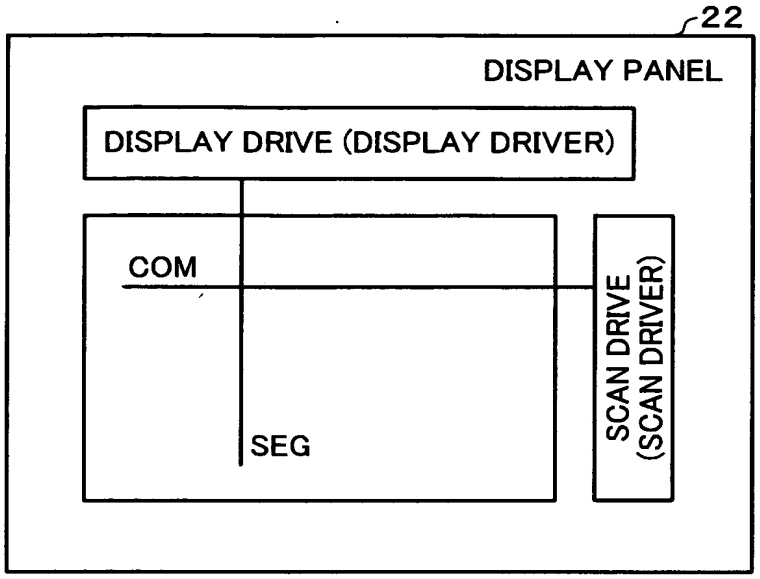


FIG 12


22
DISPLAY PANEL
DISPLAY DRIVE (DISPLAY DRIVER)
COM
SEG
SCAN DRIVE
(SCAN DRIVER)